

# Specification

## 1. Item

Product	935~960MHz 4W Solidstate Amplifier
Partnumber	ESM-0935-46-46

## 2. Revision History

Issued / Revision	R&D Approved	Revision Detail



# Specification

935~960MHz 4W Solidstate Amplifier

Design	R & D	Approval
		
CYKIM		

Customer:	Date:	Part No.: ESM-0935-46-46
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## General Specification

No.	Parameter	Specification	Unit
1	Operating Frequency Range	935~960MHz	-
2	Output Power CW	40W min.	-
3	Output Power @ 1 dB Gain Compression Point	25W min.	-
4	Output Power CDMA	4W min.	-
5	Small Signal Gain	44~48dB	-
6	Gain Flatness (ALC On)	±0.5dB	-
7	Third Order Intercept Point 2-Tones, POUT = 4 W Avg., = 500 KHz	+56dBm min. / +58dBm typ.	-
8	Input/Output Return Loss	-14dB	-
9	Noise Figure	7dB typ. / 10dB max.	-
10	Harmonics @ P1 dB Gain Compression Point	-45dBc	-
11	Spurious Signals	-70dBc typ. / -60dBc max.	-
12	Operating Voltage	26~30V	-
13	Supply Current @ POUT = 25 W CW	3.0A typ.	-
14	Supply Current @ POUT = 4 W with 2-tones	2.0A typ. / 2.5A max.	-

## Mechanical Specification

No.	Parameter	Specification	Unit
1	Dimensions	127*95.25*25.4	-
2	Weight	1.0	-
3	RF Connectors In/Out	SMA female	-
4	DC Connectors	Dsub, 9 Pins, Male	-
5	Cooling	External Heatsink	-
6	Operating Case Temperature	-10~+50°C	-
7	Storage Temperature	-40~+85°C	-
8	Relative humidity	0~95%	(non-condensing)
9	Altitude (MIL-STD-810F Method 500.4)	10,000Feet min. / 30,000Feet max.	-
10	Shock / Vibration (MIL-STD-810F Method 516.5)	Airborne typ.	-
11	Input Overdrive	+6dBm max.	-
12	Over Power Shutdown	45dBm min.	-
13	Load VSWR @ 25W output power	∞ @ all load phase & amplitude	-
14	Thermal Overload	85°C shutdown	-

## Interface Connector D-sub 9Pin

Pin No.	Description	Specifications
1	Forward Power Monitor	Continuous Analog voltage relative to forward power via RMS detector FWDM: 13 ~ 43 dBm @ 0 ~ 5 V (100 mV/dB) 28dBm output = VFWD = 2.5 VDC
2	Reverse Power Monitor	Continuous Analog voltage relative to reflected power via RMS detector REVM: 13 ~ 43 dBm @ 0 - 5V (100 mV/dB) 28dBm output = VREV = 2.5 VDC
3	ALC ON/OFF	ALC ON = TTL "Low" ALC OFF = TTL "High"
4	ALC Level	Continuous adjustable range via analog input levels Setting Point (ASP): 33 ~ 44 dBm @ 0 ~ 5 V (100 mV/dB) Error Range (AER): $\pm 1.5$ dB Response Time (ART): 100 mS/dB
5	Mute	Amplifier Enable: TTL "Low" or Open Amplifier Disable: TTL "High"
6	+VDD	+28 $\pm 2$ VDC
7	+VDD	+28 $\pm 2$ VDC
8	GND	Ground
9	GND	Ground
LED	LED Indicator	Output Power level indicator referenced to ALC setting (Independent of ALC ON or OFF)

## TYPICAL PERFORMANCE PLOTS

Top curve: Small signal gain @ Pin = -2dBm

2nd Curve: Large signal gain @ Pin = -20dBm

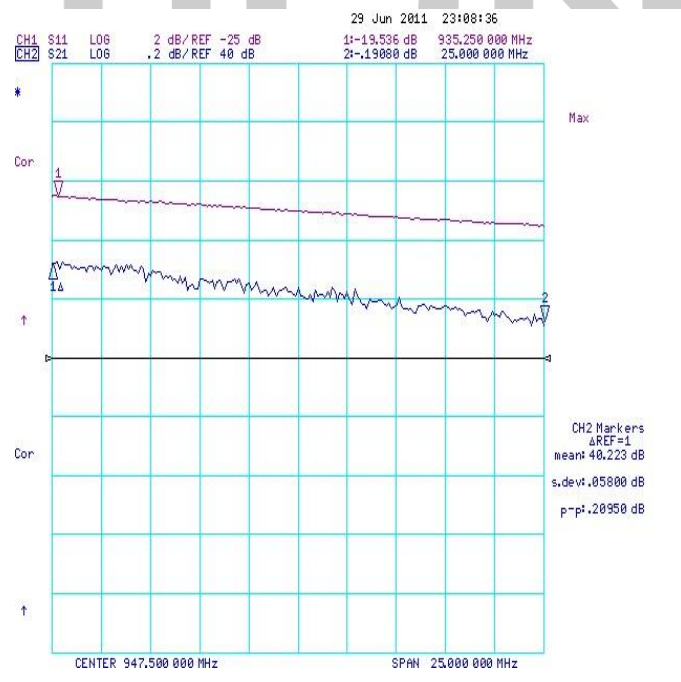
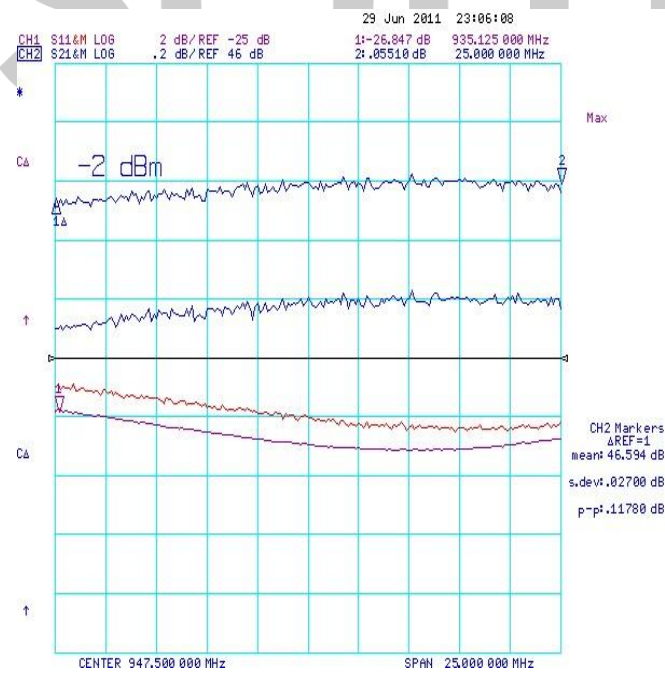
3rd Curve: Input Return Loss @ Pin = -20dBm

Bottom: Input Return Loss @ Pin = -2dBm

Top Curve: Input Return Loss

Bottom Curve: ALC Leveled @ Pout = 10W, Pin = 0dBm

Reference = 40dB

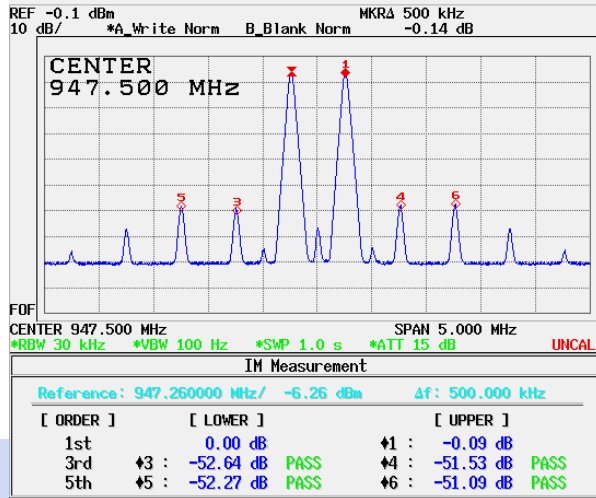


2-tone test @ Pout = 4W, Fc = 947.5MHz

Id = 1.1A

2-tone test @ Pout = 1.25W (31dBm), Fc = 947.5MHz

Id = 0.08A



Frequency

1 Center

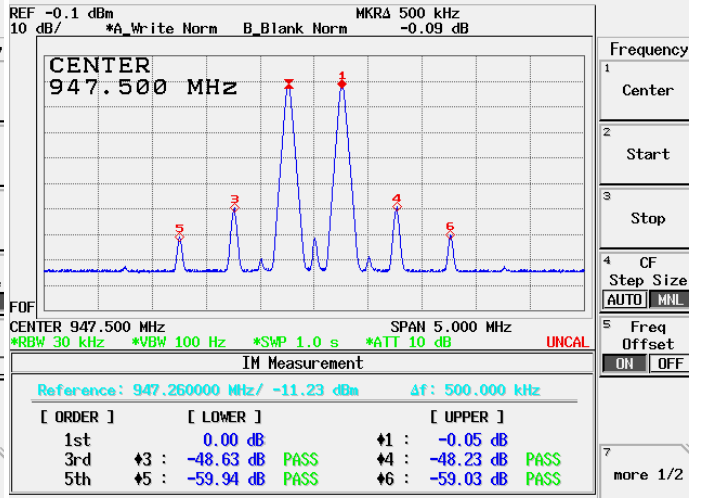
2 Start

3 Stop

4 CF Step Size [AUTO] [MNL]

5 Freq Offset [ON] [OFF]

7 more 1/2



Frequency

1 Center

2 Start

3 Stop

4 CF Step Size [AUTO] [MNL]

5 Freq Offset [ON] [OFF]

7 more 1/2

# ECHO RF SOLUTION